

**ENGINEERING CHANGE NOTE:**

ECN #:

353.1

**ASSEMBLY:** BMW4011**DESCRIPTION:** PXY CPU CARD**New Assembly:****DATE:** 21/03/01**Current Assy Rev:** H & HIGHER**Current Schematic Rev:****New Assy Rev:** UNCHANGED**New Schematic Rev:**

☐ CMS BOMs Updated ☐ Excel BOMs Updated ☐ Subcontractor Records Updated **Compatibility Maintained:** Yes

**IMPLEMENTATION INFORMATION:**

- |   |  |                                       |   |
|---|--|---------------------------------------|---|
| <input type="radio"/> Safety Issue        | <input type="radio"/> UL Compliance Issue  | <input type="radio"/> New Feature     | <input checked="" type="radio"/> Cost Reduction |
| <input type="radio"/> Bug Fix             | <input type="radio"/> EMC Compliance Issue | <input type="radio"/> Quality Issue   |   |
| <input type="radio"/> Reliability Reasons | <input type="radio"/> Software Change      | <input type="radio"/> Cosmetic Change |   |

**REASON FOR CHANGE:**

1. There is no need to pull up digital input sync clocks to allow for uninstalled QDCs. The pull up function is performed on BPR Card.
2. For operation with BPR Rev 3 backplane. (Once modified, the PXY can not be used with BPR Rev 2 or below).

**DETAILS OF CHANGE:**

1. NO NEED to piggyback network resistor to U91 as per ECN353.
2. Carefully prise off the metal shields on J2 and J6 (on solder side).  
Connect J6/B3 to J2/B20.  
Connect J6/C3 to J2/D20.  
Connect J6/E3 to J2/E20.  
Replace the metal shields.

Revision Resistors remain as per current revision.

Board Revision remains unchanged.

Note: After modified, BPR Rev 2 (B) and lower will no longer be supported.

NOTE TO SUBCONTRACTORS, PRODUCTION AND FIELD SERVICE: There is NO NEED to 'unmodify' the Cards that have already been modified as per ECN353. The mod does not affect the operation of the system.